

Module Description

Module name	Course Module
Module level, if applicable	Bachelor of Electronics Engineering
Code, if applicable	5215-xxx-x
Subtitle, if applicable	
Course, if applicable	Electronics II Practice
Semester(s) in which the module istaught	III
Person responsible for the module	Lecturer of Course
Lecturer	Dr. Wisnu Djatmiko, MT. ; Dr. Moch Sukardjo, M.Pd.
Language	Indonesian Language [Bahasa Indonesia]
Relation to Curriculum	This course is a compulsory course and offered in the 3 th semester.
Type of teaching, contact hours	<p>Teaching methods used in this course are:</p> <ul style="list-style-type: none"> - Lecture (i.e., group investigation, small group discussion, casestudy, and video based learning) - Practice (i.e., computer simulation and case study in laboratory) <p>The class size for lecture is 30 students. Contact hours for lecture is 46 hours.</p>
Workload	<p>For this course, students are required to meet a minimum of 46 hours in one semester, which consist of:</p> <ul style="list-style-type: none"> - 46 hours for lecture,
Credit points	1 credit points (equivalent with 1.44 ECTS)
Requirements according to the examination regulations	Students must have attended all classes and submitted all class assignments that are scheduled before the final tests.
Recommended prerequisites	Students must have attended all classes and submitted all class assignments that are scheduled before the final tests.

<p>PLO-CLO-ILO</p>	<p>After completing the course and given with this case:</p> <p>Course Learning Objectives (CLO1): Mahasiswa mampu menganalisis karakteristik masukan dan keluaran konfigurasi CE, CB, CC (K1, S2, S3, C2) (20)</p> <p>Course Learning Objectives (CLO2): Mahasiswa mampu merancang Titik Kerja Transistor sebagai penguat (K2, S1, S3, C1) (30)</p> <p>Course Learning Objectives (CLO3): Mahasiswa mampu menganalisis penguat daya (K1, S2, S3, C2) (20)</p> <p>Course Learning Objectives (CLO4): Mahasiswa mampu merancang Rangkaian dasar FET dan MOSFET sebagai saklar dan penguat (K2, S1, S3, C1) (30)</p> <p>Program Learning Outcome (PLO3): Menerapkan kompetensi teknik elektronika untuk memecahkan masalah keteknikan</p> <p>Attitude (A2): Untuk berkolaborasi sebagai sebuah tim, berkomunikasi secara efektif baik lisan maupun tulisan dalam lingkungan akademik dan profesional.</p> <p>Knowledge (K2): Untuk menerapkan prinsip-prinsip teknik elektronika untuk memecahkan masalah dalam sistem teknik elektronika</p> <p>Engineering and Education Skill (S1): Mampu merancang prinsip dan aplikasi sistem rekayasa elektronik</p> <p>Engineering and Education Skill (S3): Mampu mencari alternatif solusi dan pemecahan masalah di bidang teknik elektronika.</p> <p>Competence (C1): Menerapkan teknologi baru di bidang rekayasa dengan mempertimbangkan standar teknis, aspek kinerja, keandalan, penerapan, dan keberlanjutan</p> <p>Competence (C2): Mampu mengelola dan mengembangkan proses, sistem operasi, dan peralatan dengan mempertimbangkan dampak teknis dan nonteknis dari kegiatan industri di bidang teknik elektronika.</p>
---------------------------	---

Content	Students will learn about: Pratikum karakteristik masukan dan keluaran konfigurasi CE, CB, CC, Titik Kerja Transistor sebagai penguat, Penguat daya, Rangkaian dasar FET dan MOSFET sebagai saklar dan penguat.
Forms of Assessment	Assessment is carried out based on written examinations, assessment/evaluation of the learning process and performance with the following components: Sikap: 10%; Keterampilan Umum: 5%; Rancangan praktik: 15%; TI dan UAS: 60%
Study and examination requirements and forms of examination	Study and examination requirements: <ul style="list-style-type: none"> - Students must attend 15 minutes before the class starts. - Students must switch off all electronic devices. - Students must inform the lecturer if they will not attend the class due to sickness, etc. - Students must submit all class assignments before the deadline. - Students must attend the exam to get final grade. Form of examination: Written exam: Essay
Media employed	Direct Whiteboard, Google classroom, Tutorial Video by Youtube and Power Point Presentation.
Reading list	Referensi Utama: <ol style="list-style-type: none"> 1. Prinsip-Prinsip Elektronika: Albert Paul Malvino (kode: MV) 2. Rangkaian Elektronika analog: Sutarno 3. 3. Tower Transistor (data book) 4. Fakultas Teknik - UNJ, 2017, Pedoman Penyusunan Rencana Pembelajaran Semester (RPS) UNJ, Fakultas Teknik, Universitas Negeri Jakarta. Referensi Pendukung: <ol style="list-style-type: none"> 5. Moch Sukardjo dan Muhamad Yusro; Prinsip-prinsip Elektronika Dasar Pembelajaran Semester (RPS) UNJ, Fakultas Teknik, Universitas Negeri Jakarta. 6. Fakultas Teknik - UNJ, 2012, Pedoman Akademik Fakultas Teknik (FT) 2012/2013, Fakultas Teknik, Universitas Negeri Jakarta.